

## MEDIA ALERT

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### PCI-SIG Announces PCI-SIG Developers Conference 2005 in San Jose

*Conference will provide technical training for  
PCI Express 2.0 and companion specifications*

**WHAT:** PCI-SIG, the Special Interest Group responsible for PCI Express™ technology, will host the 2005 PCI-SIG Developers Conference on June 6 and 7 at the McEnergy Convention Center in San Jose, Calif. The Developers Conference will provide attendees the first technical training sessions for PCI Express (PCIe™) 2.0, as well as technical sessions addressing PCIe ExpressModule™, PCI Express 1.1 and other PCIe specifications still in development.

The conference will feature sessions targeting both the architecture necessary for successful design and the technical enabling to facilitate compliant and interoperable product development by PCI-SIG members. As in prior years, the conference will provide extensive tutorials on the core PCIe architecture as well as a comprehensive survey of the various PCIe form factors.

Member-driven implementations will be featured as a new part of the conference agenda. These sessions will describe specific implementations and may help other member companies integrate PCIe technologies seamlessly into their products. Some topics include:

- Implementing PCIe in various I/O designs
- Common pitfalls in PCIe design and verification
- Performance analysis of PCIe designs

An exhibit area will showcase the latest developments and designs based on PCI technology from these leading organizations:

- Agilent Technologies
- AMD
- ARM
- ASI SIG
- Cadence
- Catalyst Enterprises Inc.
- Denali
- FuturePlus Systems
- LeCroy
- Mentor Graphics
- NEC Electronics America, Inc.
- nSys
- NVIDIA
- PLD Applications
- PLX Technology Inc.
- Synopsys
- Tektronix Inc.
- Wavecrest

**WHEN:** June 6 - 7, 2005  
See the full conference agenda [here](#).

Members of the media/analyst community are invited to the 9 a.m. press briefing featuring PCI-SIG Chairman Tony Pierce on June 6, followed by access to the Developers Showcase and lunch. Please see the press registration link below.

**WHERE:** [McEnergy Convention Center](#)  
San Jose, California

**WHO:** The conference is hosted by the PCI-SIG and the conference's platinum sponsors:

- AMD
- Cadence Design Systems
- PLD Applications
- Synopsys

**REGISTER:** To register for the Developers Conference, employees of PCI-SIG member companies should visit the [PCI-SIG Web site](#).  
Members of the media and analyst community can register by contacting [PCI-SIG PR](#).

### **About the PCI-SIG**

The PCI-SIG is the Special Interest Group that owns and manages PCI specifications as open industry standards. The organization defines and implements new industry standard I/O (Input/Output) specifications as the industry's local I/O needs evolve. The PCI Special Interest Group was formed in 1992, and the organization became a nonprofit corporation, officially named "PCI-SIG" in the year 2000. Currently, more than 900 industry-leading companies are active PCI-SIG members worldwide. The PCI-SIG's current directors are employed by the following PCI-SIG member companies: AMD, Broadcom, HP, IBM, Intel, LSI Logic, Microsoft, Phoenix Technologies, and Texas Instruments. For more information about the PCI-SIG, and PCI-SIG membership benefits, contact the PCI-SIG by phone, at (503) 291-2569 (within the United States), or by fax at (503) 297-1090, or visit the PCI-SIG Web site at <http://www.pcisig.com>.

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