



PCI-SIG® Fact Sheet:

- PCI-SIG® membership includes more than 800 companies worldwide
- The Peripheral Component Interconnect Special Interest Group was formed in 1992 and became a nonprofit corporation named PCI-SIG in 2000
- PCI-SIG is the industry organization chartered with the development and management of the PCI™ bus specification, the industry standard for a high-performance I/O interconnect to transfer data between a CPU and its peripherals
- PCI-SIG's current directors are employed by member companies Agilent, AMD, Dell, HP, IBM, Intel, LSI, NVIDIA and Oracle

PCI Express® 3.0 Specification Fact Sheet:

- The PCI bus is one of the most successful standards in history, serving as the main general purpose bus in virtually every desktop computer throughout the world
- The PCI bus is an open and non-proprietary solution
- It addresses all areas of I/O, providing increased performance for a full range of network, disk drive, video, graphics and other high-speed peripheral devices
- PCI Express® (PCIe®) continues to be the flagship technology for I/O computing applications
- PCIe 3.0 is a low-cost, low-complexity technology
- Doubles bandwidth and optimizes power compared to PCIe 2.0
- Data rate: 8 GT/s
- Encoding layer: 128b/130b
- Backward compatible to PCIe 1.x and 2.x

**More information on PCI-SIG and PCIe 3.0 is available at www.pcisig.com
Contact us at (503)459-9150 or pr@pcisig.com**