

## Errata for PCI 2.3

This paper lists the known errata for the PCI Local Bus Specification Revision 2.3. If you have found others, please mail them to [techsupp@pcisig.com](mailto:techsupp@pcisig.com).

Figure 4-9 requires the following correction

- The phrase "All PCI Shared Signals" should be replaced with "All PCI Shared 32-bit Signals".

Section 4.4.3.1: Add the following clarification at the end of the first bullet

- The trace length of REQ64# and ACK64# (on the 32 bit connector) are limited to 1.5 inches also.

Figure 5-1 requires the following correction

- The end of the short card needs to indicate a small component-free edge similar to the top edge of the card. Also an additional arrow needs to point to the end of the short card indicating it has a component-free area on both sides. The size of this component-free area is the same as the top edge: 1 mm (.039 inches).
- Move the lower bracket hole farther from the end of the card so it is in line with the upper bracket hole.
- On the connector end view the dimension 0.35mm indicating the location of the chassis opening from the top of the component side of the add in card should refer the reader to figure 5-11 detail A.

Figure 5-2 requires the following correction

- The dimension between the bracket mounting holes in Figure 2 and Figure 3 is not correct. It should be changed to 85.4 [3.362].

Figure 5-3 requires the following corrections

- The hole spacing of 85.4 mm is correct but the inches value should be 3.362
- The dimension between the bracket mounting holes in Figure 2 and Figure 3 is not correct. It should be changed to 85.4 [3.362].
- 6.600" MIN dimension for length should be removed. Only use the 6.600" MAX dimension.

Figure 5-4 requires the following correction

- The allowed component height on side B is listed as 4.48 [.570] and needs to be changed to 14.48 [.570]
- The holes for mounting the brackets are shown as triangles. They should be round.

Figure 5-6 requires the following correction

- Move the lower bracket hole farther from the end of the card so it is in line with the upper bracket hole. ← --- **Formatted: Bullets and Numbering**

Figure 5-7 requires the following corrections.

- The dimension 0.37mm in the end view should be 0.35mm. Similar to Figure 5-1 it should reference a Detail view on the bracket drawing Figure 5-8.
- In the end view change the 12.07 [.475] dimension to 12.06 [.475].
- Datum C (in the card, top view) needs to be moved from the back side of the card to the component side of the card. ← --- **Formatted: Bullets and Numbering**

Figure 5-8 requires the following correction

- The width of the tab at 0.563" is incorrect. The dimension for the tab should be  $0.563" - 0.162" = 0.401"$ .
- A Detail view should be added similar to Detail A on Figure 5-11 to clarify the width and location of the chassis connector opening in reference to the add in card component side. These dimensions should match those on Figure 5-11, 0.35mm and 12.06mm.

Figure 5-11 requires the following correction

- This figure shows three views of the bracket and an enlarged view in Detail A. A correction is needed in the center view. The dimension 12.06 [.47] needs to be replaced with 12.06 [.475] and the left dimension line is not in the right place. The left dimension needs to be moved to the right so it will be in line with the left side of the cross hatched area.
- The top left view needs to show the lower bracket mounting hole lining up with the upper bracket mounting hole.
- Detail A requires the following addition. Add the 12.06mm dimension from the end view to Detail A.

Figure 5-13 is incorrect.

- It is replaced by the following figure (this is the same as Figure 5-19 from the PCI 2.2 specification).

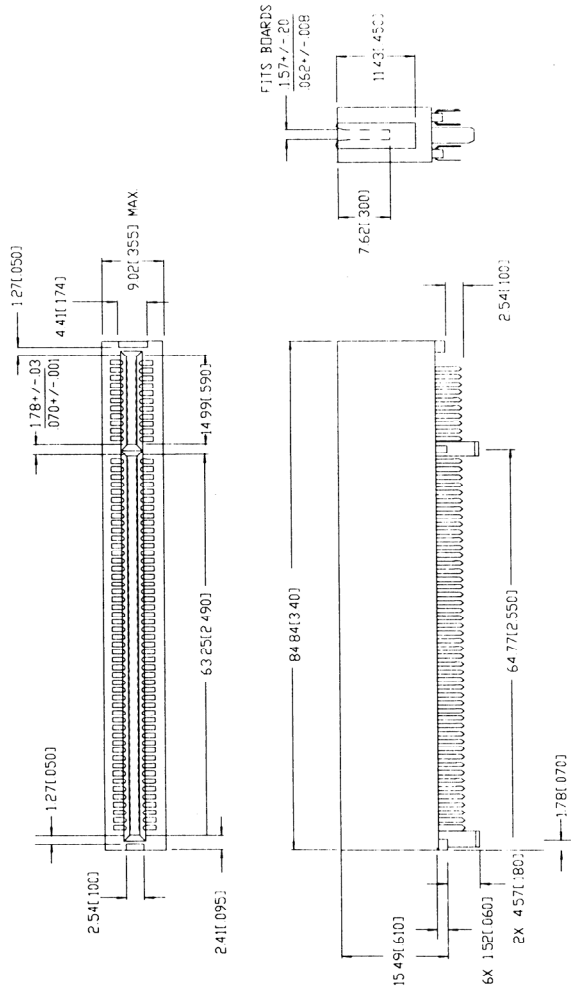


Figure 5-13: 32-bit Connector



Figure 5-18 requires the following correction

- In the top view of figure 5-18 the dimension 41.86 [1.648] is correct but it is shown extending beyond the end of the connector. It needs to point to the end of the slot in the connector as shown in figure 5-24 in the PCI 2.2 spec.

Figure 5-20 requires the following correction

- The width of the notch in figure 5-20 is correct at 0.927 +/- 0.025 mm but the inches dimension is incorrect. It should be 0.0365 +/- 0.001 inches.

Figure 5-21 requires the following correction

- The width of the notch in figure 5-21 is correct at 0.927 +/- 0.025 mm but the inches dimension is incorrect. It should be 0.0365 +/- 0.001 inches.

Figure 5-22 requires the following correction

- The width of the notch in figure 5-22 is correct at 0.927 +/- 0.025 mm but the inches dimension is incorrect. It should be 0.0365 +/- 0.001 inches.

Figure 5-23 requires the following correction

- The width of the notch in figure 5-23 is correct at 0.927 +/- 0.025 mm but the inches dimension is incorrect. It should be 0.0365 +/- 0.001 inches.

Figure 5-26 requires the following correction

- The primary 5V PCI Primary Reference Datum should be centered between the pins so that they will line up with the 5V key in the universal card.

Figure 5-30 requires the following correction

- The 4.57(.180) dimension should be measured from the bottom of the spacer as in figure 5-13. In the top view of figure 5-30 the dimension 58.17 [2.290] is correct but it is shown extending beyond the end of the connector. It needs to point to the end of the slot in the connector.

| ▼ ----- Deleted: Figure 5-61 requires the following correction¶  
¶  
<#>Move the lower bracket hole farther from the end of the card so it is in line with the upper bracket hole.¶