



PCI-SIG ENGINEERING CHANGE NOTICE

TITLE:	PCI _OSC Clarifications
DATE:	July 21, 2006
AFFECTED DOCUMENT:	PCI Firmware Specification, Revision 3.0
SPONSOR:	Dong Wei; Hewlett Packard Company

Part I

1. Summary of the Functional Changes

This ECN attempts to make clarifications for several situations:

- a) _OSC return values for the Control Field bits when they are placed under PCI/PCI-X or PCI Express hierarchies when some of them may not apply
- b) The OS determines whether a slot supports hot plug by checking the slot status when it claims native hot plug.
- c) PCI/PCI-X and PCI Express mixed platform's _OSC settings
- d) _OSC Control field bits dependencies

2. Benefits as a Result of the Changes

This ECN attempts to make clarifications such that the system firmware and multiple operating systems can have the same interpretation of the specification and work interoperably.

3. Assessment of the Impact

Different interpretations of the current PCI _OSC definition resulted in different implementations of _OSC among firmware and operating systems vendors, resulting in interoperability issues. This ECR helps to clarify on the specifications, so all implementations can be adjusted to the same interpretation, makes it possible for interoperable implementations.

4. Analysis of the Hardware Implications

No impact to underlying hardware.

5. Analysis of the Software Implications

This ECN potentially requires changes to the existing implementations.

Part II

Detailed Description of the change in the associated file.