



PCI-SIG ENGINEERING CHANGE NOTICE

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|---------------------------|------------------------------------|
| TITLE: | +3Vaux Typo correction |
| DATE: | January <u>12</u> , 2004 |
| AFFECTED DOCUMENT: | PCI Express Mini Card, Release 1.0 |
| SPONSOR: | Ron Shaw, Dell, Inc. |

Part I

1. Summary of the Functional Changes

1. Correct typo in section 3.2.1 left after addition of +3Vaux pin

2. Benefits as a Result of the Changes

Correct error in the specification to match text found elsewhere.

3. Assessment of the Impact

None.

4. Analysis of the Hardware Implications

N/A.

5. Analysis of the Software Implications

N/A.

Part II

Detailed Description of the change

Change section 3.2.1 to the following:

3.2.1 Power Sources and Grounds

PCI Express Mini Card provides ~~three~~ power sources: ~~two~~ at 3.3V (~~+3.3V and +3.3Vaux~~) and one at 1.5V (+1.5V). The auxiliary voltage source (+3.3Vaux) ~~may be the only supply voltage~~ available during the system's stand-by/suspend state to support wake event processing on the communications card.

Deleted: two

Deleted: one

Deleted: is sourced over the same pins as the primary voltage (+3.3V) and is